

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sheng Hung Wang

Divisional Application No.: Not Yet Assigned

Filed: November 12, 2003

For: EFFICIENT BUS UTILIZATION IN A  
MULTIPROCESSOR SYSTEM BY  
DYNAMICALLY MAPPING  
MEMORY ADDRESSES

Examiner: Unassigned

Art Group: Unassigned

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-identified application, please consider the following amendments and remarks.

**IN THE SPECIFICATION**

On page 1, after the title and before the background section, please insert the following:

**-- CROSS-REFERENCES TO RELATED APPLICATIONS**

This is a Divisional Application of U.S. Patent Application No. 09/691,391, filed October 18, 2000. This Divisional Application claims the benefit of the U.S. Patent Application No. 09/691,391. --

**IN THE CLAIMS**

Please cancel claims 4-7, 11-14, 18-21.